

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes et al.

Title: SILICON CARBIDE GATE TRANSISTOR

Docket No.: 303.326US1

Filed: July 29, 1997

Examiner: Donghee Kang

Serial No.: 08/903486

Due Date: N/A

Group Art Unit: 2811

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450



We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard.
- ☒ A Supplemental Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.), and copies of cited documents.
- ☒ A check in the amount of \$180.00 to cover the fee for consideration of Information Disclosure Statement under 97(c).

If not provided for in a separate paper filed herewith, Please consider this a **PETITION FOR EXTENSION OF TIME** for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
Customer Number 21186

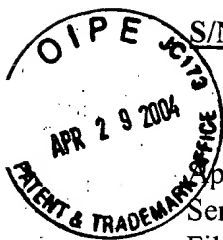
By: David R. Cochran  
Atty: David R. Cochran  
Reg. No. 46,632

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 27 day of April, 2004.

MEREDITH MESCHER  
Name

Meredith Mescher  
Signature

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
(GENERAL)



S/N 08/903486

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Leonard Forbes et al.	Examiner:	Donghee Kang
Serial No.:	08/903,486	Group Art Unit:	2811
Filed:	July 29, 1997	Docket:	303.326US1
Title:	SILICON CARBIDE GATE TRANSISTOR		

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(c)(2), Applicants have included the fee of \$180.00 as set forth in 37 C.F.R. §1.17(p). Please charge any additional fees or credit any overpayment to Deposit Account No. 19-0743.

04/30/2004 FHETEK11 00000046 08903486

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Serial No :08/903486

Filing Date: July 29, 1997

Title: SILICON CARBIDE GATE TRANSISTOR

Page 2

Dkt: 303.326US1

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

LEONARD FORBES ET AL.

By their Representatives,

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Date 27 APRIL 2004

By

David R. Cochran  
David R. Cochran  
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MEREDITH MESCHER  
Name

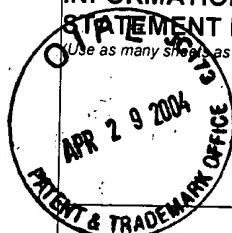
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Signature

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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Sheet 1 of 1

Complete if Known

Application Number	08/903,486
Filing Date	July 29, 1997
First Named Inventor	Forbes, Leonard
Group Art Unit	2811
Examiner Name	Kang, Donghee

Attorney Docket No: 303.326US1

**US PATENT DOCUMENTS**

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
	US-5,336,361	08/09/1994	Tamura, A., et al.	438	767	11/02/1992
	US-5,990,531	11/23/1999	Taskar, N. R., et al.	257	410	11/12/1997
	US-6,166,768	12/26/2000	Fossum, et al.	348	308	01/22/1997

**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T <sup>2</sup>
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**OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
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**EXAMINER****DATE CONSIDERED**

Substitute Disclosure Statement Form (PTO-1449)

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional) <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached